EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(("6199031") or ("5732247")).PN.	USPAT; USOCR	OR	OFF	2006/07/13 14:39
L2	2	"20030078762"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/13 14:39
S1	16	(CPU same model same simulat$4) and (interface$ and debug$4) and (functional adj model) and (CPU same bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/13 14:18

Google Scholar ™ BETA

[simulator + CPU + interface + bus + hardware] [Search]

Scholar Results **1 - 10** of about **914** for <u>simulator</u> + <u>CPU</u> + <u>interface</u> + <u>bus</u> + <u>hardware</u> + "<u>functional mode</u>

<u>Methodology for **hardware**/software co-verification in C/C++(short</u>   **All articles** <u>Recent articles</u>
<u>paper)</u>
L. Séméria, A Ghosh - Proceedings of the 2000 conference on Asia South Pacific ..., 2000 - portal.acm.org
... blocks, abstract models with proper **interface** behavior are ... has been targeted to a
processor, models used ... in conjunction with an instruction set **simulator** (ISS ...
<u>Cited by 70</u> - <u>Web Search</u>

<u>Simics: A full system simulation platform</u> - <u>group of 5 »</u>
PS Magnusson, M Christensson, J Eskilson, D ... - Computer, 2002 - ieeexplore.ieee.org
... An object corresponds to a processor or device in the target ... VHDL **simulator** ... Disks
Memory management unit Command-line **interface** Scripting Tracing Debugger ...
<u>Cited by 189</u> - <u>Web Search</u> - <u>BL Direct</u>

<u>COSY Communication IP's</u> - <u>group of 10 »</u>
JY Brunel, WM Kruijtzer, H Kenter, F Pétrot, L ... - Proc. Design Automation Conf, 2000 -
doi.ieeecomputersociety.org
... the arbitration and call-back delays are added at **simulation** time, such that the
results account for the contention on ... RAM **CPU** MIPS ... Slave Wrapper VCI **interface** ...
<u>Cited by 63</u> - <u>Web Search</u> - <u>BL Direct</u>

<u>Miami: a **hardware** software co-simulation environment</u> - <u>group of 4 »</u>
R Klein - Proceedings of the 7th IEEE International Workshop on Rapid ..., 1996 - doi.ieeecs.org
... edge, giving us 240,000 processor model evaluations in ... of the integrated **hardware**
simulator and instruction ... the instruction set model/**bus interface** model with ...
<u>Cited by 19</u> - <u>Web Search</u>

<u>**Hardware**/software selected cycle solution</u> - <u>group of 3 »</u>
J Wilson - Hardware/Software Codesign, 1994., Proceedings of the Third ..., 1994 - ieeexplore.ieee.org
... have started to use **hardware** modellers to **interface** real-life processors to the
simulation model. The object code can execute on the target processor. ...
<u>Cited by 9</u> - <u>Web Search</u>

<u>**Hardware**/software co-simulation in a VHDL-based test bench approach</u> - <u>group of 7 »</u>
M Bauer, W Ecker - Proc. of the Design Automation Conference, 1997 - doi.ieeecomputersociety.org
... distinguishes in the kind of processor model: Software ... propagation delay is considered
at the **interface**. ... method between software and **simulator** [Bec92, Rom96 ...
<u>Cited by 13</u> - <u>Web Search</u> - <u>BL Direct</u>

<u>Model refinement for **hardware**-software codesign</u> - <u>group of 8 »</u>
J Gong, DD Gajski, S Bakshi - ACM Transactions on Design Automation of Electronic Systems ..., 1997 -
portal.acm.org
... such as partitioning, estima- tion, **simulation**, and prototyping ... designer to trade
off **CPU** cost for ... protocol, **bus** arbiter, and **bus interface** introduced after ...
<u>Cited by 23</u> - <u>Web Search</u> - <u>Library Search</u>

<u>Scalable and flexible cosimulation of SoC designs with heterogeneous multi-processor target</u>
<u>...</u> - <u>group of 9 »</u>
P Gerin, S Yoo, G Nicolescu, AA Jerraya - Proceedings of the 2001 conference on Asia South Pacific ..., 2001 -